Oluseyi (Seyi) Ayorinde is the team lead for the Silicon Technologies team at the DEVCOM Army Research Lab. He is currently working out of the west coast office in Playa Vista. He received his PhD in Electrical Engineering from the University of Virginia, where he explored generating and configuring custom, sub-threshold FPGA hardware, as well as designing accelerators for ultra-low power Systems-on-Chip (SoCs). He is currently focusing on development of low-power digital circuits for various applications. Dr. Ayorinde has taped out 8 chips, three of which were specifically designed to accelerate AI/ML algorithms for edge processing. Dr. Ayorinde has also been called on as a subject matter expert for numerous DARPA Microsystems Technology Office (MTO) programs focused on integrated circuit design. His research interests include Machine Learning Acceleration, Digital ASIC design, and Low-Power FPGA hardware design.